

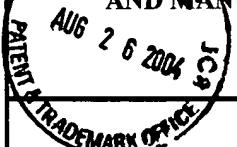
TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
16782

In Re Application Of: Yoshihisa Matsubara, et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/608,647	June 27, 2003	Roy Karl Potter	23389	2822	8331

Title: SEMICONDUCTOR APPARATUS OF WHICH RELIABILITY OF INTERCONNECTIONS IS IMPROVED
AND MANUFACTURING METHOD OF THE SAME



Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

- the statement specified in 37 CFR 1.97(e);

OR

- the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))**

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**SEMICONDUCTOR APPARATUS OF WHICH RELIABILITY OF INTERCONNECTIONS IS
AND MANUFACTURING METHOD OF THE SAME**

AUG 26 2004



Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

A check in the amount of _____ is attached.

The Director is hereby authorized to charge and credit Deposit Account No. **19-1013/SSMP** as described below.

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Aasheesh V. Shravah

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Dated: August 24, 2004

Aasheesh V. Shravah

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yoshihisa Matsubara, et al.

Examiner: Roy Karl Potter

Serial No.: 10/608,647

Art Unit: 2822

Filed: June 27, 2003

Docket: 16782

For: SEMICONDUCTOR APPARATUS OF
WHICH RELIABILITY OF
INTERCONNECTIONS IS IMPROVED AND
MANUFACTURING METHOD OF THE SAME

Dated: August 24, 2004

Conf. No.: 8331

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 23313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R §§1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Laid-Open Patent Application No. 6-177128, dated June 24, 1994;
2. Japanese Laid-Open Patent Application No. 6-97164, dated April 8, 1994;

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 24, 2004.

Dated: August 24, 2004

Aasheesh V. Shravah

3. Japanese Laid-Open Patent Application No. 2002-75995, dated March 15, 2002; and
4. Japanese Laid-Open Patent Application No. 11-102909, dated April 13, 1999.

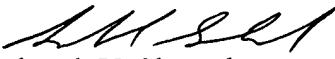
The references were cited in an Office Action dated August 3, 2004 received from the Japanese Patent Office. Applicants are submitting copies of the above-cited references, together with an English translation of the Examiner's comments regarding the references from the Official Action. The relevance of the references is described in the Official Action.

In compliance with the requirements of 37 C.F.R. §1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 37 C.F.R. §1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the references were cited. The relevance to the pending U.S. patent application is that the references were cited in a foreign patent application on the same subject matter. However, no independent analysis of the references, the accuracy of the statement of the foreign examiner or the claims of the foreign application under the laws of that country or the United States relative to the subject matter claimed in the present application has been made; the present understanding of the contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted herewith.

The undersigned attorney hereby states that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent

office in a counterpart foreign application not more than three months prior to the filing of this
Information Disclosure Statement.

Respectfully submitted,


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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

AUG 26 2004

Docket Number (Optional)

16782

Application Number

10/608,647

Applicant(s)

Yoshihisa Matsubara, et al.

Filing Date

June 27, 2003

Group Art Unit

2822

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		6-177128	6/24/1994	Japan				
		6-97164	4/8/1994	Japan				
		2002-75995	3/15/2002	Japan				
		11-102909	4/13/1999	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER _____ **DATE CONSIDERED** _____

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.